

## AMENDMENTS TO THE CLAIMS

**Claim 1 (Currently Amended)**      A data reception device for receiving a data packet containing a plurality of packets, each packet of the plurality of packets having first time information added thereto, and ~~wherein~~ the first time information regarding a time at which the plurality of packets are generated is added to each of the plurality of packets, the data reception device comprising:

a receive data buffer unit for storing the plurality of packets contained in the data packet;

a receive data amount measuring unit for measuring ~~a data~~ an amount of data stored in the receive data buffer unit to obtain a measured value;

a variable clock generation unit for generating a clock having a variable ~~whose~~ frequency ~~is variable~~;

a time information output unit for outputting second time information that is counted in accordance with a frequency of the clock generated by the variable clock generation unit; and

a first time information comparison unit for comparing the first time information added to the plurality of packets with the second time information outputted from the time information output unit, the second time information indicating a time that is behind by a predetermined offset time, and for controlling a timing of outputting the plurality of the packets stored in the receive data buffer unit, ~~wherein~~

wherein the receive data amount measuring unit controls a the frequency of the  
clock generated by the variable clock generation unit in accordance with ~~values~~ the measured  
value obtained by the receive data amount measuring unit, and

          wherein the time information output unit comprises:

          a timer unit for outputting third time information that is counted in  
accordance with the frequency of the clock generated by the variable clock generation unit;

          a time information extraction unit for extracting the first time information  
added to the plurality of packets stored in the receive data buffer unit;

          an initialization unit for coinciding the third time information outputted by  
the timer unit with the first time information extracted by the time information extraction unit;  
and

          an offset unit for delaying, by the predetermined offset time, the third time  
information output by the time unit to obtain the second time information and for outputting the  
second time information.

**Claim 2 (Cancelled)**

**Claim 3 (Cancelled)**

**Claim 4 (Currently Amended)**     The data reception device according to ~~claim 3~~ claim 1,  
wherein the offset unit changes the predetermined offset time in accordance with a condition-  
~~conditions~~ of a transmission line to which ~~where~~ the data reception device is connected.

**Claim 5 (Currently Amended)** The data reception device according to ~~claim 3~~ claim 1, wherein the offset unit changes the predetermined offset time in accordance with ~~a data~~ an amount of data accumulated in the receive data buffer unit.

**Claim 6 (Currently Amended)** The data reception device according to ~~claim 3~~ claim 1, wherein the offset unit changes the predetermined offset time in accordance with a type ~~kind~~ of a communication protocol.

**Claim 7 (Currently Amended)** The data reception device according to ~~claim 3~~ claim 1, further comprising a second time information comparison unit for comparing the third time information outputted by the timer unit with the first time information extracted by the time information extraction unit and for controlling, based on a result of the comparison ~~result~~, a, the frequency of the clock generated by the variable clock generation unit.

**Claim 8 (Currently Amended)** The data reception device according to claim 7, wherein, until a predetermined time from a time point of starting the reception of the data packet has passed, ~~a~~, the frequency of the clock generated by the variable clock generation unit is controlled by the second time information comparison unit, and

wherein, after the predetermined time from the time point of starting the reception of the data packet has passed, the frequency of the clock generated by the variable clock generation unit is controlled by the receive data amount measuring unit.

**Claim 9 (Currently Amended)** The data reception device according to ~~claim 3~~ claim 1, wherein after a predetermined time has passed, the receive data amount measuring unit starts controlling ~~[[a]]~~ the frequency of the clock generated by the variable clock generation unit ~~after predetermined time has passed~~.

**Claim 10 (Currently Amended)** The data reception device according to claim 1, wherein the receive data amount measuring unit controls the frequency of the clock generated by the variable clock generation unit by using an average value, ~~as a measured value~~, of data amounts accumulated in the receive data buffer unit, the average value being used as the measured value.

**Claim 11 (Currently Amended)** The data reception device according to claim 1, wherein the plurality of packets contained in the data packet are packets that are in an MPEG2-2TS format ~~packets~~.

**Claim 12 (Currently Amended)** The data reception device according to claim 1, wherein as the data packet, audio signals and video signals are encoded to be generated as a plurality of packets that are in an MPEG2-TS format, ~~packets to each of the packets having the~~ which first time information regarding a time at of the encoding time ~~is added thereto~~, and wherein the ~~plurality of packets that are in the MPEG2-TS format and that have packets having the added~~ first time information added thereto are combined and transmitted from the data transmission device which is operable to generate the data packet.

**Claim 13 (Currently Amended)** An integrated circuit for receiving a data packet containing a plurality of packets, each packet of the plurality of packets having first time information added thereto, and the, wherein first time information regarding a time at which the plurality of packets are generated ~~is added to each of the plurality of packets,~~ the integrated circuit comprising:

a receive data amount measuring unit for measuring a data an amount of data stored in a receive data buffer unit, ~~which that~~ is connected to the integrated circuit, ~~for storing~~ and that stores the plurality of packets contained in the data packet, the measured amount of data being used to obtain a measured value;

a variable clock generation unit for generating a clock ~~whose~~ having a variable frequency is variable;

a time information output unit for outputting second time information that is counted in accordance with a frequency of the clock generated by the variable clock generation unit; and

a first time information comparison unit for comparing the first time information added to the plurality of packets with the second time information outputted by the time information output unit, the second time information indicating a time that is behind by a predetermined offset time, and for controlling a timing of outputting the plurality of packets stored in the receive data buffer unit, ~~wherein~~

wherein the receive data amount measuring unit controls ~~a the~~ frequency of the clock generated by the variable clock generation unit in accordance with ~~values~~ the measured value obtained by the receive data amount measuring unit, and

wherein the time information output unit comprises:

a timer unit for outputting third time information that is counted in  
accordance with the frequency of the clock generated by the variable clock generation unit;

a time information extraction unit for extracting the first time information  
added to the plurality of packets stored in the receive data buffer unit;

an initialization unit for coinciding the third time information outputted by  
the timer unit with the first time information extracted by the time information extraction unit;  
and

an offset unit for delaying, by the predetermined offset time, the third time  
information output by the time unit to obtain the second time information and for outputting the  
second time information.